PATENT 29925/37672

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TPE			
70, , , %	Application of: Young-hun Bae and)	I hereby certify that this paper and
, mar g	Application of: Young-hun Bae and Won-sung Park Serial No.: 09/940 774)	the documents referred to as
APR O A	ម្លាំ ទី៖)	enclosed therewith are being
THAD COLUMN	Serial No.: 09/940,774)	deposited with the United States
	Filed: August 27, 2001)	Postal Service as first class mail,
)	postage prepaid, on March 28,
	For: Method for Forming a Gate of a High Integration Semiconductor Device)	2002, in an envelope addressed to
)	Commissioner for Patents,
)	Washington, D.C. 20231.
	Group Art Unit: 2811) /)	
	Examiner: To be assigned)	Michael R. Hull
		j	Reg. No./35,902/
		Ś	Aftorney for Applicants

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

The patents listed on the enclosed PTO/SB/08 are submitted pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. Copies of the patents are enclosed as necessary.

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first office action on the merits. In accordance with 37 C.F.R. § 1.97(b), no certification or fee is required.

The Commissioner is authorized to charge any deficiency required by this paper, or credit any overpayment, to Deposit Account No. 13-2855.

Respectfully submitted,

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March 28, 2002

By: